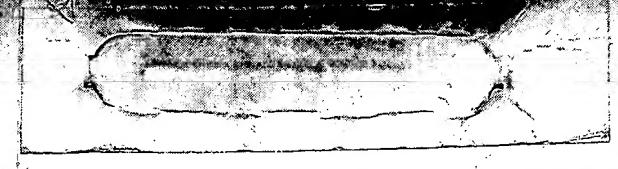


J10000 U.S. PTO  
09/852839  
05/10/01

713	Subless
324	Class
ISSUE CLASSIFICATION	



PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.  
HM 15-3  
SCANNED 15-3 a.a. Am

PATENT DATE

APPLICATION NO. 09/852839	CONT/PRIOR F	CLASS 438 324	SUBCLASS 158 58	ART UNIT 2812 2816	EXAMINER 1st Review, Nguyen Vincent
APPLICANTS Masataka Kondo Kiyoto Ohta Tomonori Fujimoto	324 713 2816 58				
TITLE Semiconductor integrated circuit, test method for the same, and recording device and communication equipment having the same					PTO-2040 12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
324	713	324	719		
INTERNATIONAL CLASSIFICATION		360	46		
G01R 27/08					
H01L 21/31					

Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	18	20	1	18	1
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____				NOTICE OF ALLOWANCE MAILED	
				09/09/03	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				ISSUE FEE	
				Amount Due	Date Paid
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				9/10/03	

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